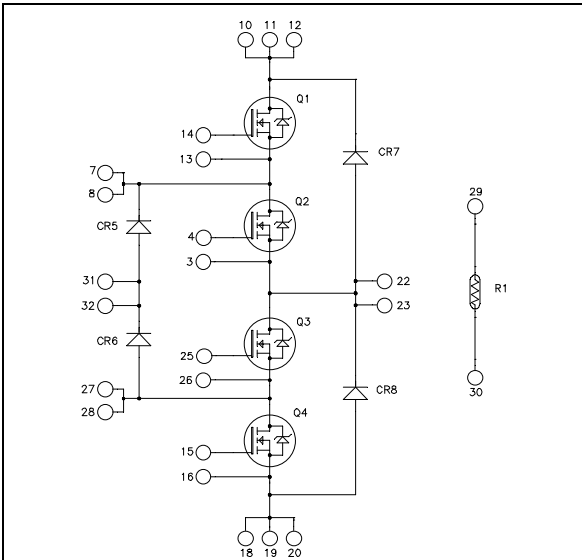


Three level inverter
SiC MOSFET Power Module

SiC Power MOSFET :
 $V_{DSS} = 1200V$; $R_{DSon} = 98m\Omega$ @ $T_j = 25^\circ C$

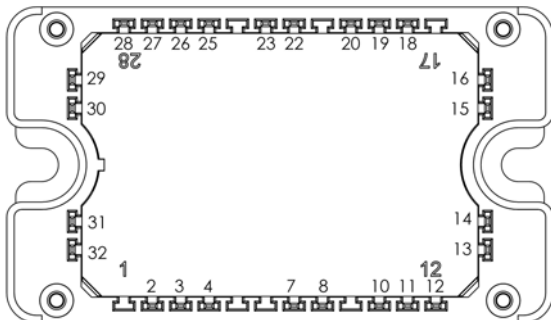


Application

- Uninterruptible Power Supplies

Features

- **SiC Power MOSFET**
 - Low $R_{DS(on)}$
 - High temperature performance
- **SiC Schottky Diode**
 - Zero reverse recovery
 - Zero forward recovery
 - Temperature Independent switching behavior
 - Positive temperature coefficient on VF
- Kelvin emitter for easy drive
- Very low stray inductance
- High level of integration
- Internal thermistor for temperature monitoring
- AlN substrate for improved thermal performance



Benefits

- Stable temperature behavior
- Very rugged
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Low profile
- RoHS Compliant

All multiple inputs and outputs must be shorted together
 10/11/12 ; 7/8 ; 27/28 ; ...

All ratings @ $T_j = 25^\circ C$ unless otherwise specified

Q1 to Q4 Absolute maximum ratings (per SiC MOSFET)

Symbol	Parameter	Max ratings	Unit
V_{DSS}	Drain - Source Voltage	1200	V
I_D	Continuous Drain Current	$T_c = 25^\circ C$	28
		$T_c = 80^\circ C$	22
I_{DM}	Pulsed Drain current	55	A
V_{GS}	Gate - Source Voltage	-10/+23	V
V_{GSOP}	Gate - Source Voltage ; recommended operation values	-5/+18	
R_{DSon}	Drain - Source ON Resistance	98	m Ω
P_D	Power Dissipation	$T_c = 25^\circ C$	125
			W

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

Q1 to Q4 Electrical Characteristics (per SiC MOSFET)

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
I _{DSS}	Zero Gate Voltage Drain Current	V _{GS} = 0V, V _{DS} = 1200V			100	μA
R _{DS(on)}	Drain – Source on Resistance	V _{GS} = 20V; I _D = 20A T _j = 25°C		80	98	mΩ
		V _{GS} = 18V; I _D = 20A T _j = 175°C		153		
V _{GS(th)}	Gate Threshold Voltage	V _{GS} = V _{DS} , I _D = 5mA	2	2.6	4	V
I _{GSS}	Gate – Source Leakage Current	V _{GS} = 20 V, V _{DS} = 0V			250	nA

Q1 to Q4 Dynamic Characteristics (per SiC MOSFET)

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
C _{iss}	Input Capacitance	V _{GS} = 0V V _{DS} = 1000V f = 1MHz		950		pF
C _{oss}	Output Capacitance			80		
C _{rss}	Reverse Transfer Capacitance			7.6		
Q _g	Total gate Charge	V _{GS} = -5/20V V _{Bus} = 800V I _D = 20A		62		nC
Q _{gs}	Gate – Source Charge			15		
Q _{gd}	Gate – Drain Charge			23		
T _{d(on)}	Turn-on Delay Time	V _{GS} = -2/+20V V _{Bus} = 800V I _D = 20A R _L = 40Ω ; R _G = 50Ω		12		ns
T _r	Rise Time			14		
T _{d(off)}	Turn-off Delay Time			23		
T _f	Fall Time			18		
E _{on}	Turn on Energy	Inductive Switching V _{GS} = -5/+20V V _{Bus} = 600V I _D = 20A R _G = 50Ω		T _j = 150°C	0.45	mJ
E _{off}	Turn off Energy			T _j = 150°C	0.25	
R _{Gint}	Internal gate resistance			4.6		Ω
R _{thJC}	Junction to Case Thermal Resistance				1	°C/W

Source - Drain diode ratings and characteristics (per SiC MOSFET)

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
V _{SD}	Diode Forward Voltage	V _{GS} = 0V I _{SD} = 10A	T _j = 25°C	3.3		V
			T _j = 175°C	3.1		
t _{rr}	Reverse Recovery Time	I _{SD} = 20A ; V _{GS} = -5V V _R = 800V ; di _F /dt = 2400A/μs		32		ns
Q _{rr}	Reverse Recovery Charge			192		nC
I _{rr}	Reverse Recovery Current			10		A

CR5 & CR6 SiC diode ratings and characteristics (Per SiC diode)

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
V _{RRM}	Peak Repetitive Reverse Voltage				600	V
I _{RM}	Reverse Leakage Current	V _R = 600V	T _j = 25°C	10	60	μA
			T _j = 175°C	20	300	
I _F	DC Forward Current		T _c = 125°C	10		A
V _F	Diode Forward Voltage	I _F = 10A	T _j = 25°C	1.6	1.8	V
			T _j = 175°C	2	2.4	
Q _C	Total Capacitive Charge	I _F = 10A, V _R = 600V di/dt = 500A/μs		28		nC
C	Total Capacitance	f = 1MHz, V _R = 200V		65		pF
		f = 1MHz, V _R = 400V		50		
R _{thJC}	Junction to Case Thermal Resistance				2.2	°C/W

CR7 & CR8 SiC diode ratings and characteristics (Per SiC diode)

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
V _{RRM}	Peak Repetitive Reverse Voltage				1200	V
I _{RM}	Reverse Leakage Current	V _R = 1200V	T _j = 25°C	64	400	μA
			T _j = 175°C	112	2000	
I _F	DC Forward Current		T _c = 125°C	20		A
V _F	Diode Forward Voltage	I _F = 20A	T _j = 25°C	1.6	1.8	V
			T _j = 175°C	2.3	3	
Q _C	Total Capacitive Charge	I _F = 20A, V _R = 1200V di/dt = 1000A/μs		160		nC
C	Total Capacitance	f = 1MHz, V _R = 200V		192		pF
		f = 1MHz, V _R = 400V		138		
R _{thJC}	Junction to Case Thermal Resistance				0.8	°C/W

Temperature sensor NTC (see application note APT0406 on www.microsemi.com).

Symbol	Characteristic	Min	Typ	Max	Unit
R ₂₅	Resistance @ 25°C		50		kΩ
ΔR ₂₅ /R ₂₅			5		%
B _{25/85}	T ₂₅ = 298.15 K		3952		K
ΔB/B	T _C = 100°C		4		%

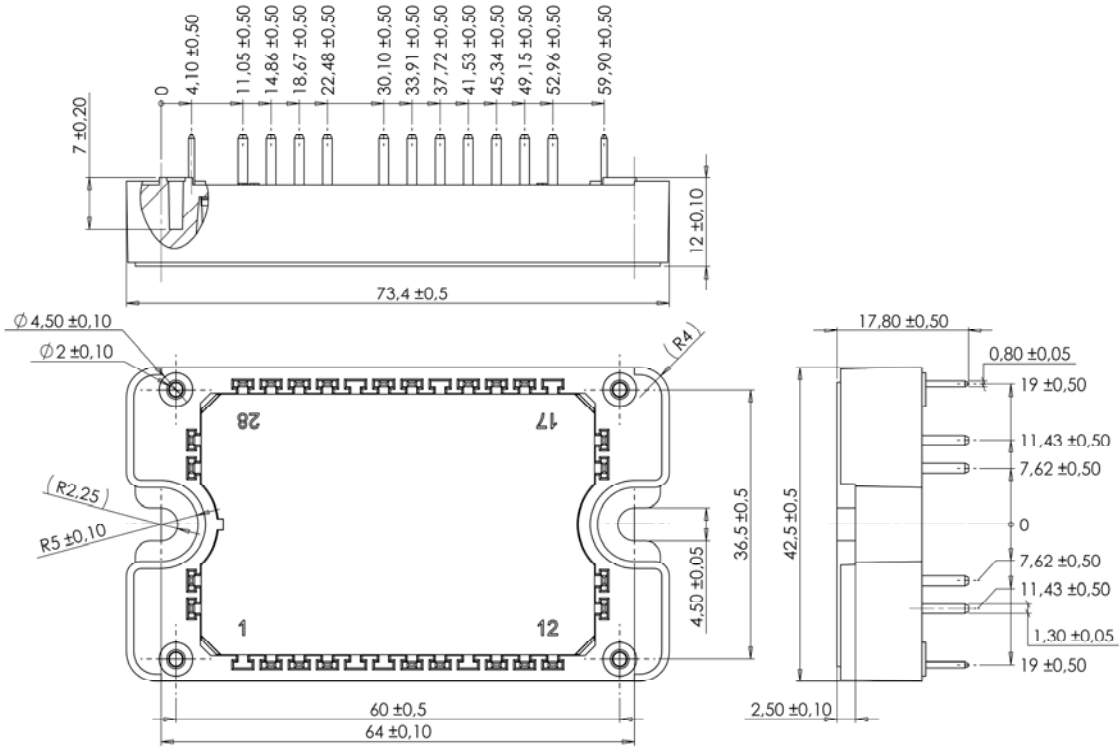
$$R_T = \frac{R_{25}}{\exp \left[B_{25/85} \left(\frac{1}{T_{25}} - \frac{1}{T} \right) \right]}$$

T: Thermistor temperature
R_T: Thermistor value at T

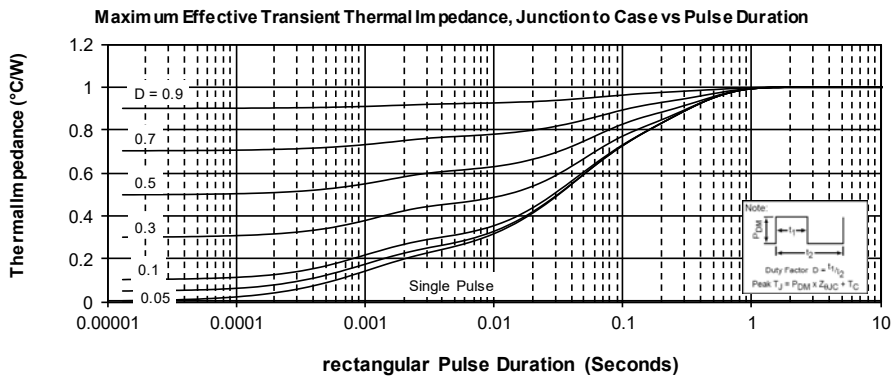
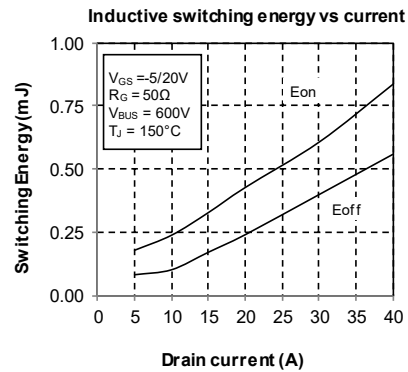
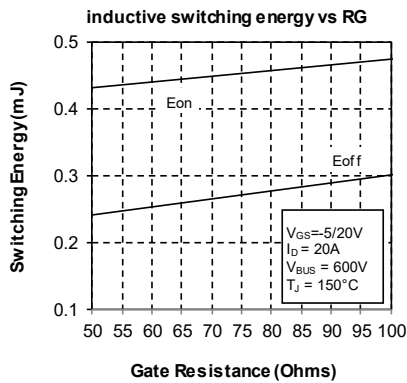
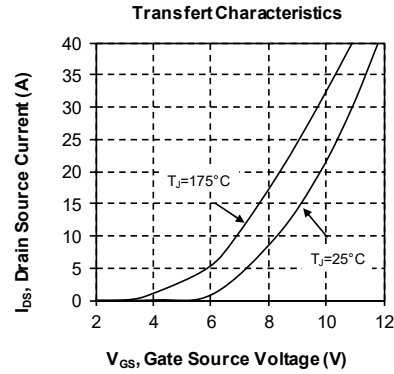
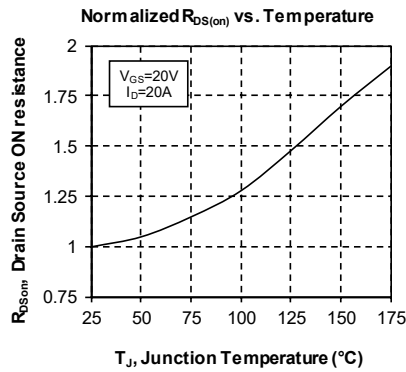
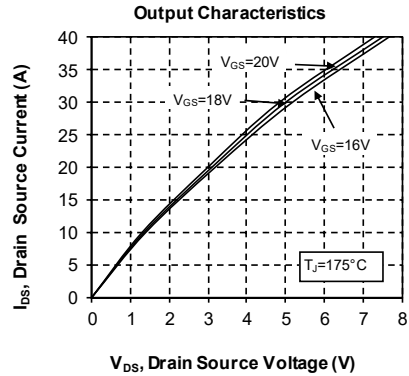
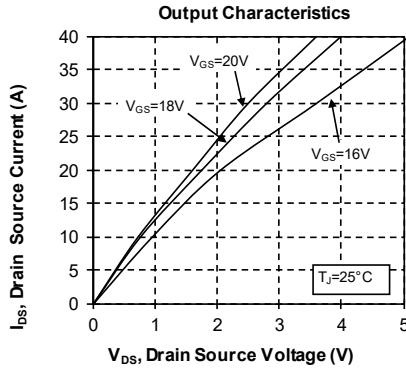
Thermal and package characteristics

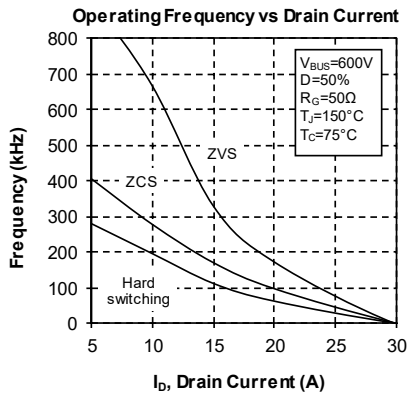
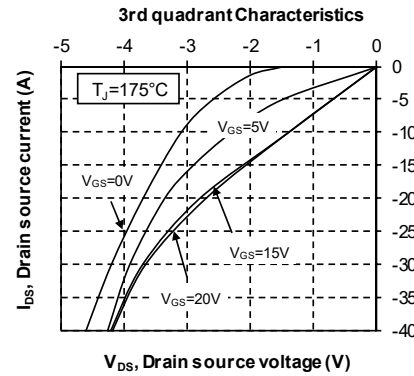
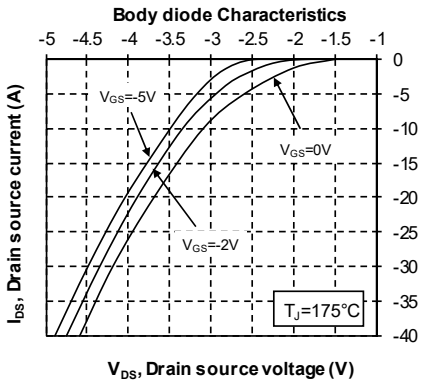
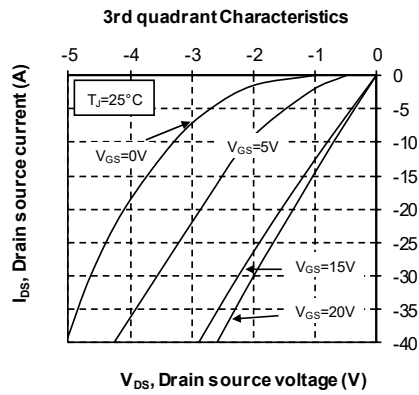
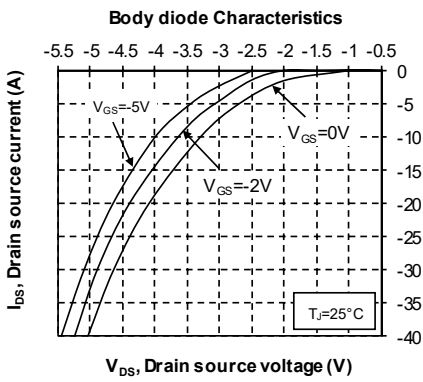
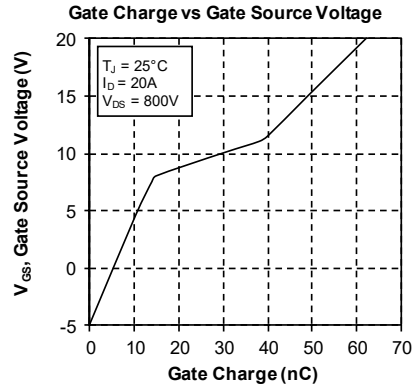
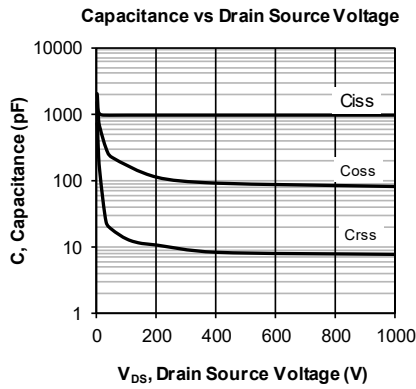
Symbol	Characteristic	Min	Max	Unit		
V _{ISOL}	RMS Isolation Voltage, any terminal to case t = 1 min, 50/60Hz	4000		V		
T _J	Operating junction temperature range	-40	175	°C		
T _{JOP}	Recommended junction temperature under switching conditions	-40	T _{Jmax} - 25			
T _{STG}	Storage Temperature Range	-40	125			
T _C	Operating Case Temperature	-40	125			
Torque	Mounting torque	To heatsink	M4	2	3	N.m
Wt	Package Weight				110	g

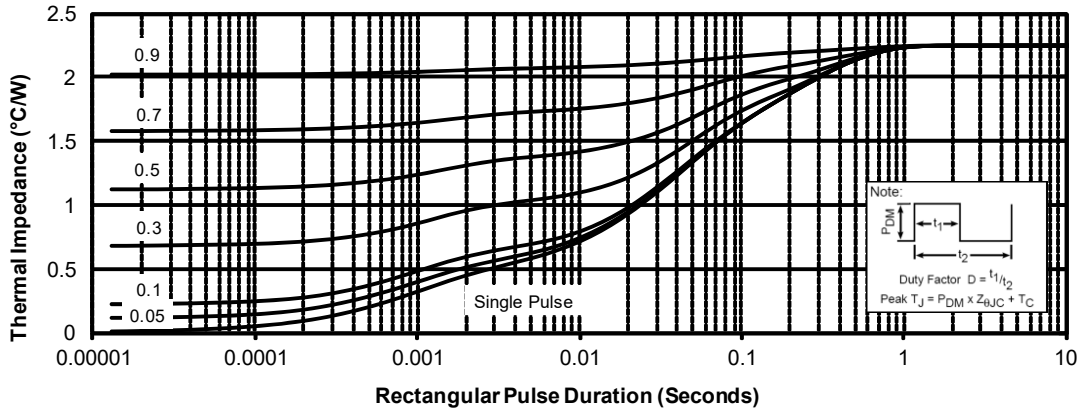
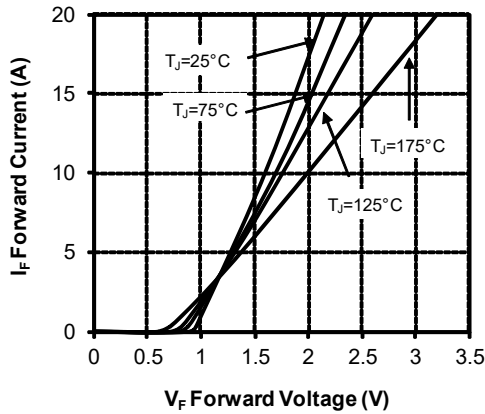
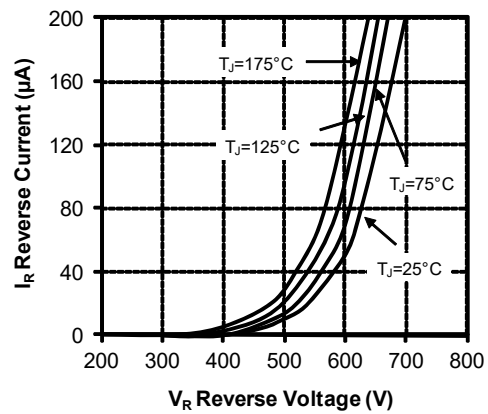
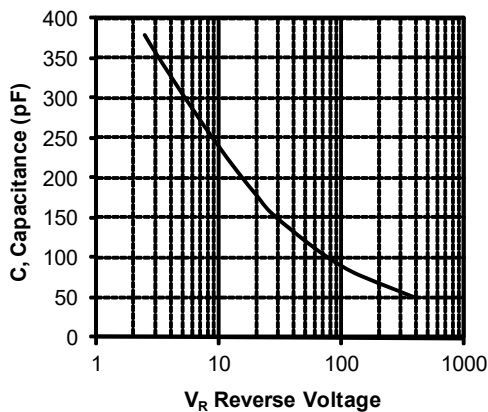
Package outline (dimensions in mm)

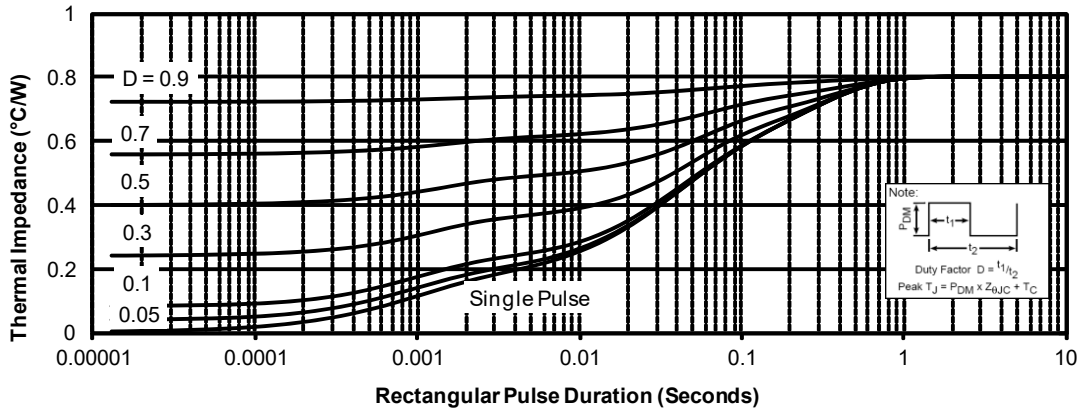
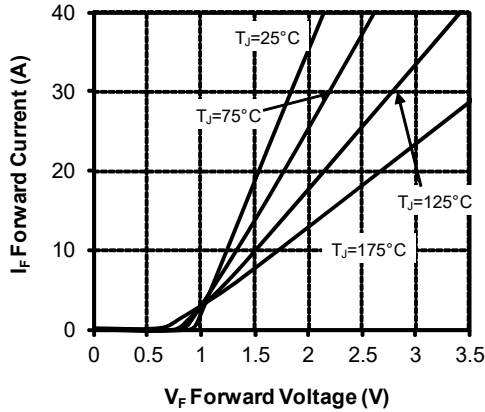
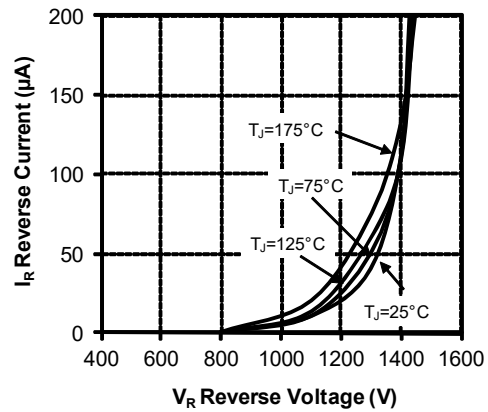
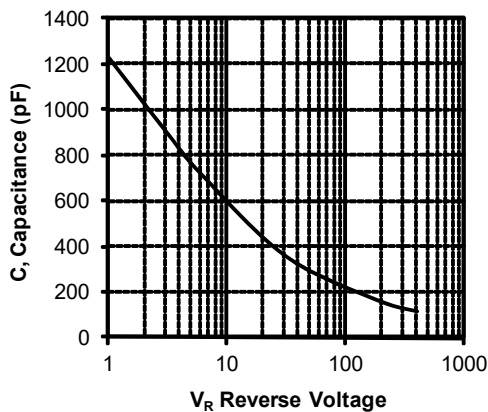


See application note 1906 - Mounting Instructions for SP3F Power Modules on www.microsemi.com

Q1 to Q4 Typical performance curve




CR5 & CR6 Typical performance curve
Maximum Effective Transient Thermal Impedance, Junction to Case vs Pulse Duration

Forward Characteristics

Reverse Characteristics

Capacitance vs. Reverse Voltage


CR7 & CR8 Typical performance curve
Maximum Effective Transient Thermal Impedance, Junction to Case vs Pulse Duration

Forward Characteristics

Reverse Characteristics

Capacitance vs. Reverse Voltage


DISCLAIMER

The information contained in the document (unless it is publicly available on the Web without access restrictions) is PROPRIETARY AND CONFIDENTIAL information of Microsemi and cannot be copied, published, uploaded, posted, transmitted, distributed or disclosed or used without the express duly signed written consent of Microsemi. If the recipient of this document has entered into a disclosure agreement with Microsemi, then the terms of such Agreement will also apply. This document and the information contained herein may not be modified, by any person other than authorized personnel of Microsemi. No license under any patent, copyright, trade secret or other intellectual property right is granted to or conferred upon you by disclosure or delivery of the information, either expressly, by implication, inducement, estoppels or otherwise. Any license under such intellectual property rights must be approved by Microsemi in writing signed by an officer of Microsemi.

Microsemi reserves the right to change the configuration, functionality and performance of its products at anytime without any notice. This product has been subject to limited testing and should not be used in conjunction with life-support or other mission-critical equipment or applications. Microsemi assumes no liability whatsoever, and Microsemi disclaims any express or implied warranty, relating to sale and/or use of Microsemi products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright or other intellectual property right. Any performance specifications believed to be reliable but are not verified and customer or user must conduct and complete all performance and other testing of this product as well as any user or customers final application. User or customer shall not rely on any data and performance specifications or parameters provided by Microsemi. It is the customer's and user's responsibility to independently determine suitability of any Microsemi product and to test and verify the same. The information contained herein is provided "AS IS, WHERE IS" and with all faults, and the entire risk associated with such information is entirely with the User. Microsemi specifically disclaims any liability of any kind including for consequential, incidental and punitive damages as well as lost profit. The product is subject to other terms and conditions which can be located on the web at <http://www.microsemi.com/legal/tnc.asp>

Life Support Application

Seller's Products are not designed, intended, or authorized for use as components in systems intended for space, aviation, surgical implant into the body, in other applications intended to support or sustain life, or for any other application in which the failure of the Seller's Product could create a situation where personal injury, death or property damage or loss may occur (collectively "Life Support Applications").

Buyer agrees not to use Products in any Life Support Applications and to the extent it does it shall conduct extensive testing of the Product in such applications and further agrees to indemnify and hold Seller, and its officers, employees, subsidiaries, affiliates, agents, sales representatives and distributors harmless against all claims, costs, damages and expenses, and attorneys' fees and costs arising, directly or indirectly, out of any claims of personal injury, death, damage or otherwise associated with the use of the goods in Life Support Applications, even if such claim includes allegations that Seller was negligent regarding the design or manufacture of the goods.

Buyer must notify Seller in writing before using Seller's Products in Life Support Applications. Seller will study with Buyer alternative solutions to meet Buyer application specification based on Sellers sales conditions applicable for the new proposed specific part.