PI74ALVCHR162245

3.3V 16-Bit Bidirectional Transceiver with 3-State Output

Product Features

- PI74ALVCHR162245 is designed for low voltage operation
- Vcc = 2.3V to 3.6V
- Hysteresis on all inputs
- Typical V_{OLP} (Output Ground Bounce) < 0.8V at V_{CC} = 3.3V, T_A = 25°C
- Typical V_{OHV} (Output V_{OH} Undershoot) < 2.0V at V_{CC} = 3.3V, T_A = 25°C
- All output ports have equivilent 26Ω for overshoot/ undershoot protection series resistors: No external resistors are required
- Bus Hold retains last active bus state during 3-state eliminating the need for external pull-up resistors
- Industrial operation at -40°C to +85°C
- Packages available:
  – 48-pin 240-mil wide plastic TSSOP (A)

Product Description

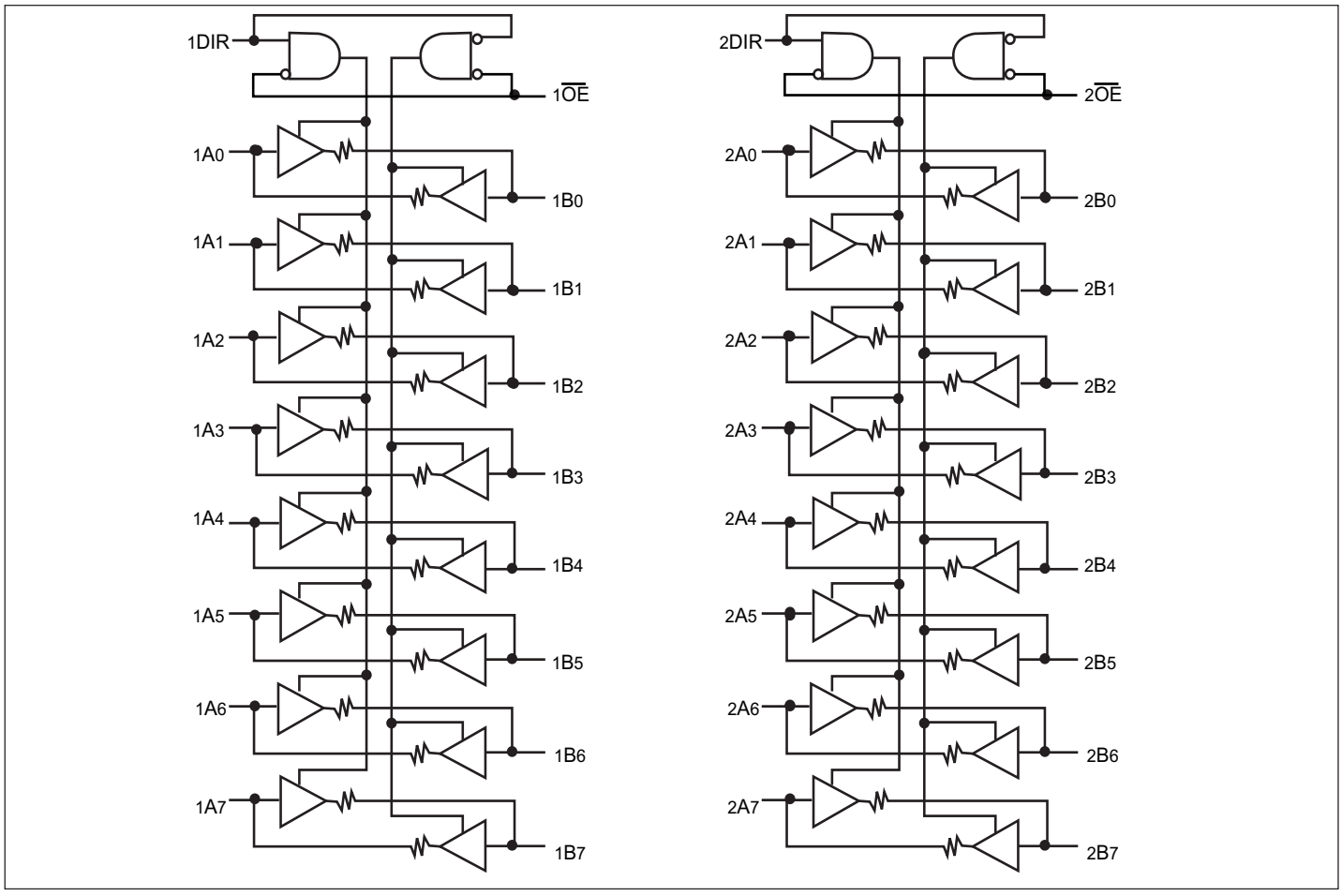
Pericom Semiconductor's PI74ALVCH series of logic circuits are produced using the Company's advanced 0.5 micron CMOS technology to achieve industry-leading speed grades.

The PI74ALVCHR162245 is a 16-bit bidirectional transceiver designed for asynchronous two-way communication between data buses. The direction control input pin (xDIR) determines the direction of data flow through the bidirectional transceiver. The Direction and Output Enable controls are designed to operate this device as either two independent 8-bit transceivers or one 16-bit transceiver. The output enable ($\overline{OE}$) input, when HIGH, disables both A and B ports by placing them in HIGH Z condition.

To ensure the high-impedance state during power up or power down, $\overline{OE}$ should be tied to Vcc through a pull-up resistor; the minimum value of the resistor is determined by the current sinking ability of the driver.

Active bus-hold circuitry is provided to hold unused or floating data inputs at a valid logic level.

Logic Block Diagram



Product Pin Description

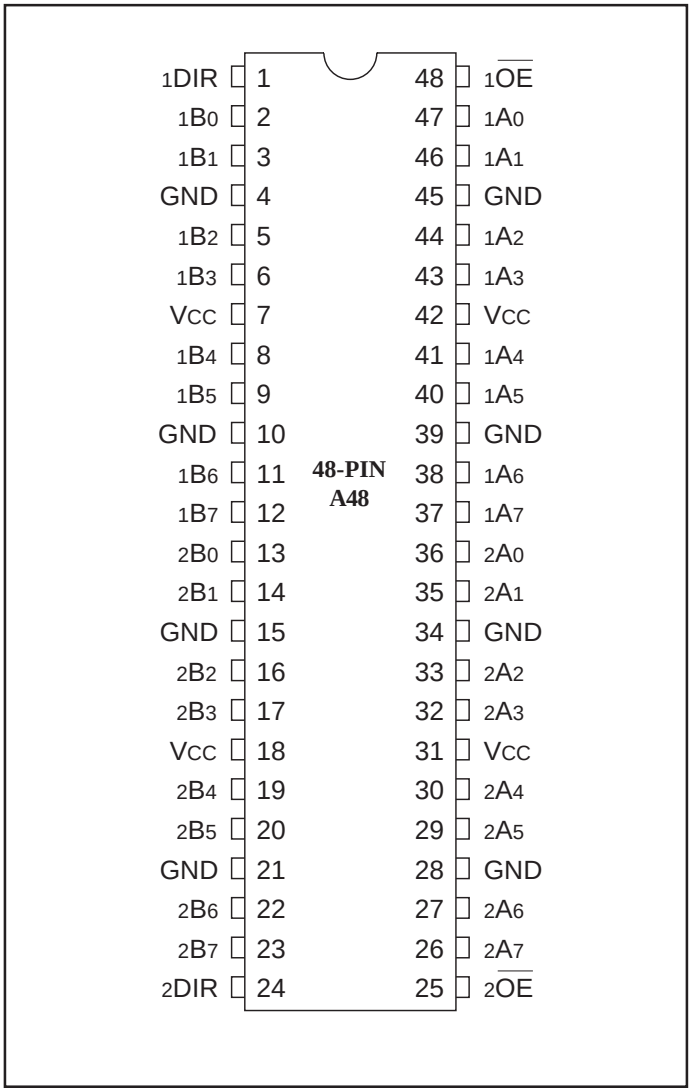
Pin Name	Description
x$\overline{OE}$	3-State Output Enable Inputs (Active LOW)
xDIR	Direction Control Input
xAx	Side A Inputs or 3-State Inputs
xBx	Side B Outputs or 3-State Outputs
GND	Ground
VCC	Power

Truth Table(1)

Inputs		Outputs
x$\overline{OE}$	**xDIR**	
L	L	Bus B Data to Bus A
L	H	Bus A Data to Bus B
H(2)	X	Z

Note:
1. H = High Voltage Level, X = Don't Care, L = Low Voltage Level, Z = High Impedance
2. Resistor to V_{CC}

Product Pin Configuration



Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	–65°C to +150°C
Ambient Temperature with Power Applied	–40°C to +85°C
Input Voltage Range, VIN	–0.5V to +4.6V
Output Voltage Range, VOUT	–0.5V to V_{CC} +0.5V
DC Input Voltage	–0.5V to +5.0V
DC Output Current	50 mA
Power Dissipation	1.0W

Note:
Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics (Over the Operating Range, T_A = –40°C to +85°C, V_{CC} = 3.3V ± 10%)

Parameters	Description	Test Conditions[1]			Min.	Typ.[2]	Max.	Units
V_{CC}	Supply Voltage				2.3		3.6	V
V_{IH}[3]	Input HIGH Voltage	V_{CC} = 2.3V to 2.7V			1.7			
		V_{CC} = 2.7V to 3.6V			2.0			
V_{IL}[3]	Input LOW Voltage	V_{CC} = 2.3V to 2.7V					0.7	
		V_{CC} = 2.7V to 3.6V					0.8	
V_{IN}[3]	Input Voltage				0		V_{CC}	
V_{OUT}[3]	Output Voltage				0		V_{CC}	
V_{OH}	Output HIGH Voltage	I_{OH} = -100 µA, 2.3V to 3.6V			Vcc -0.2			
		I_{OH} = -4mA	V_{IH} = 1.7V	2.3V	1.9			
			V_{IH} = 2V	2.7V	2.2			
		I_{OH} = -6mA	V_{IH} = 1.7V	2.3V	1.7			
			V_{IH} = 2V	3V	2.4			
		I_{OH} = -8mA	V_{IH} = 2V	2.7V	2			
		I_{OH} = -12mA	V_{IH} = 2V	3V	2			
V_{OL}	Output LOW Voltage	I_{OL} = 100µA, 2.3V to 3.6V					0.2	
		I_{OL} = 4mA	V_{IL} = 0.7V	2.3V			0.4	
			V_{IH} = 2V	2.7V			0.4	
		I_{OL} = 6mA	V_{IL} = 0.7V	2.3V			0.55	
			V_{IL} = 0.8V	3V			0.55	
		I_{OL} = 8mA	V_{IL} = 0.8V	2.7V			0.6	
		I_{OL} = 12mA	V_{IL} = 0.8V	3V			0.8	
I_{OH}[3]	Output HIGH Current	V_{CC} = 2.3V					-6	mA
		V_{CC} = 2.7V					-8	
		V_{CC} = 3.0V					-12	
I_{OL}[3]	Output LOW Current	V_{CC} = 2.3V					6	
		V_{CC} = 2.7V					8	
		V_{CC} = 3.0V					12	
I_{IN}	Input Current	V_{IN} = V_{CC} or GND, V_{CC} = 3.6V					±5	µA
I_{IN} (I_{HOLD})	Input Hold Current	V_{IN} = 0.7V, V_{CC} = 2.3V			45			
		V_{IN} = 1.7V, V_{CC} = 2.3V			-45			
		V_{IN} = 0.8V, V_{CC} = 3.0V			75			
		V_{IN} = 2.0V, V_{CC} = 3.0V			-75			
		V_{IN} = 0 to 3.6V, V_{CC} = 3.6V					±500	
I_{OZ}	Output Current (3-State Outputs)	V_{OUT} = VCC or GND, V_{CC} = 3.6V					±10	
Icc	Supply Current	V_{CC} = 3.6V, I_{OUT} = 0µA, V_{IN} = GND or V_{CC}					40	
ΔIcc	Supply Current per Input @ TTL HIGH	V_{CC} = 3.0V to 3.6V One Input at V_{CC} - 0.6V Other Inputs at V_{CC} or GND					750	
C_I	Control Inputs	V_{IN} = V_{CC} or GND, V_{CC} = 3.3V				4		pF
C_{IO}	A or B Ports	V_O = V_{CC} or GND, V_{CC} = 3.3V				9		

Notes:
1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at Vcc = 3.3V, +25°C ambient and maximum loading.
3. Unused Control Inputs must be held HIGH or LOW to prevent them from floating.

Switching Characteristics over Operating Range[1]

Parameters	From (INPUT)	To (OUTPUT)	V_{CC} = 2.5V ± 0.2V		V_{CC} = 2.7V		V_{CC} = 3.3V ± 0.3V		Units
			Min.[2]	Max.	Min.[2]	Max.	Min.[2]	Max.	
t_{PD}	A or B	B or A	1.0	4.9		4.7	1.0	4.2	ns
t_{EN}	$\overline{OE}$		1.0	6.8		6.7	1.0	5.6	
t_{DIS}	$\overline{OE}$		1.0	6.3		5.7	1.0	5.5	
	Description								
$\Delta t/\Delta v$[3]	Input Transition Rise or Fall		0	10	0	10	0	10	ns/V

Notes:
1. See test circuit and waveforms.
2. Minimum limits are guaranteed but not tested on Propagation Delays.
3. Recommended operating condition.

Operating Characteristics, T_A = 25°C

Parameters		Test Conditions	V_{CC} = 2.5V ± 0.2V	V_{CC} = 3.3V ± 0.3V	Units
			Typ.	Typ.	
Cpd Power Dissipation Capacitance	Outputs Enabled	C_L = 50pF, f = 10 MHz	24	32	pF
	Outputs Disabled		4	5	